

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	15	(US-20020038439-\$).did. or (US-6496886-\$ or US-7072818-\$ or US-6466898-\$ or US-6377911-\$ or US-6564367-\$ or US-6279146-\$ or US-6377912-\$ or US-6810442-\$ or US-6842729-\$ or US-6732068-\$ or US-6694464-\$ or US-7050962-\$ or US-6754185-\$ or US-5329470-\$). did.	US-PGPUB; USPAT	OR	ON	2007/03/08 15:20
L2	11	I1 and (signal with direction)	US-PGPUB; USPAT	OR	ON	2007/03/08 15:20
L3	0	I2 and (compare near direction)	US-PGPUB; USPAT	OR	ON	2007/03/08 15:20
L4	0	I2 and (compare with direction)	US-PGPUB; USPAT	OR	ON	2007/03/08 15:20
L5	0	fuji-mototsugu\$.in. and @pd>"20060706"	US-PGPUB; USPAT; EPO	OR	ON	2007/03/08 15:50
L6	0	tada-osamu\$.in. and @pd>"20060706"	US-PGPUB; USPAT; EPO	OR	ON	2007/03/08 15:50
L7	416007	morimoto-kazunobu\$.in. @pd>"20060706"	US-PGPUB; USPAT; EPO	OR	ON	2007/03/08 15:51
L8	416007	morimoto-kazunobu\$.in. @pd>"20060706"	US-PGPUB; USPAT; EPO	OR	ON	2007/03/08 15:51
L9	329608	morimoto-kazunobu\$.in. @pd>"20060706"	US-PGPUB; USPAT	OR	ON	2007/03/08 15:51
L10	1186	I9 and (bridge adj circuit)	US-PGPUB; USPAT	OR	ON	2007/03/08 15:51
L11	1	I10 and (logic adj simulat\$3)	US-PGPUB; USPAT	OR	ON	2007/03/08 15:52
L12	1	I10 and (logic adj emulat\$3)	US-PGPUB; USPAT	OR	ON	2007/03/08 15:52
L13	2	yamagiwa-akira\$.in. and @pd>"20060706"	US-PGPUB; USPAT	OR	ON	2007/03/08 15:53
L14	0	nanao-hisashi\$.in. and @pd>"20060706"	US-PGPUB; USPAT	OR	ON	2007/03/08 15:54
L15	3634	renesas adj technology\$.as.	US-PGPUB; USPAT	OR	ON	2007/03/08 15:54
L16	17	I15 and (bridge adj circuit)	US-PGPUB; USPAT	OR	ON	2007/03/08 15:55

## EAST Search History

L17	1	I16 and ((logic adj simulat\$3) or (logic adj emulat\$3))	US-PGPUB; OR USPAT	ON	2007/03/08 15:55
L18	26846	(simulat\$3 or emulat\$3) and @pd>"20060706"	US-PGPUB; OR USPAT	ON	2007/03/08 15:58
L19	4748	I18 and accelerat\$3	US-PGPUB; OR USPAT	ON	2007/03/08 15:58
L20	2739	I18 and (bi\$directional or two\$way)	US-PGPUB; OR USPAT; EPO	ON	2007/03/08 15:59
L21	1	I20 and drivability	US-PGPUB; OR USPAT; EPO	ON	2007/03/08 15:59
L22	1	I20 and driveability	US-PGPUB; OR USPAT; EPO	ON	2007/03/08 15:59
L23	1262	I20 and ((bridge or interface) with user)	US-PGPUB; OR USPAT; EPO	ON	2007/03/08 16:00
L24	374	(logic adj verification)	US-PGPUB; OR USPAT	ON	2007/03/08 16:00
L25	103	I24 and simulator	US-PGPUB; OR USPAT	ON	2007/03/08 16:00
L26	21	I25 and ("FPGA" or (field adj programmable adj gate adj array))	US-PGPUB; OR USPAT	ON	2007/03/08 16:01
L27	8	I26 and (interface with user)	US-PGPUB; OR USPAT	ON	2007/03/08 16:03
L28	3	I26 and (interface adj circuit)	US-PGPUB; OR USPAT	ON	2007/03/08 16:03
L29	146	703/14.ccls. and @pd>"20060706"	US-PGPUB; OR USPAT	ON	2007/03/08 16:11
S1	310	simulation near accelerat\$3	US-PGPUB; OR USPAT	ON	2006/07/06 06:49
S2	101	S1 and (FPGA or (field adj programmable adj gate adj array))	US-PGPUB; OR USPAT	ON	2006/07/05 17:05
S3	65	S2 and ((two near way) or (bi\$directional))	US-PGPUB; OR USPAT	ON	2006/07/05 17:05
S4	3002	renesas adj technology\$.as.	US-PGPUB; OR USPAT; EPO	ON	2006/07/06 06:50
S5	487	S4 and (simulat\$3 or verify or verification or emulat\$3)	US-PGPUB; OR USPAT; EPO	ON	2006/07/06 06:51
S6	16	S5 and accelerator	US-PGPUB; OR USPAT; EPO	ON	2006/07/06 06:57

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S7	1	fuji-mototsugu\$.in.	US-PGPUB; OR USPAT; EPO	ON	2007/03/08 15:50
S9	36	tada-osamu\$.in.	US-PGPUB; OR USPAT; EPO	ON	2007/03/08 15:50
S11	6	morimoto-kazunobu\$.in.	US-PGPUB; OR USPAT; EPO	ON	2007/03/08 15:50
S12	69	yamagiwa-akira\$.in.	US-PGPUB; OR USPAT; EPO	ON	2007/03/08 15:52
S20	292992	simulat\$3 or emulat\$3	US-PGPUB; OR USPAT; EPO	ON	2007/03/08 15:57
S22	16424	S21 and (bi\$directional or two\$way)	US-PGPUB; OR USPAT; EPO	ON	2007/03/08 15:58
S40	7	(logic near simulat\$3) and (bridge near circuit)	US-PGPUB; OR USPAT	ON	2007/03/08 09:51
S41	543	(logic near simulat\$3) and (interface with user)	US-PGPUB; OR USPAT	ON	2007/03/08 09:51
S42	427	(logic near simulat\$3) and (interface with circuit)	US-PGPUB; OR USPAT	ON	2007/03/08 09:51
S43	536931	S42 amd accelerat\$3	US-PGPUB; OR USPAT	ON	2007/03/08 09:52
S44	101	S42 and accelerat\$3	US-PGPUB; OR USPAT	ON	2007/03/08 09:52
S45	24	S44 and ("FPGA" or (field adj programmable adj gate adj array))	US-PGPUB; OR USPAT	ON	2007/03/08 09:54
S46	12	S45 and (connect\$2 near direct\$2)	US-PGPUB; OR USPAT	ON	2007/03/08 09:54